

Efficient On-Board Charger to Improve the Life Time of Electric Vehicle Battery

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ABSTRACT

Internal combustion engines produce about 10% of the world's greenhouse gas emissions. Electric vehicles generate 17-30% lower emissions than the internal combustion engines. However, the formers entail certain drawbacks, namely the few available charging stations, the high charging cost, and the limited battery life. The purpose of this paper is to propose the best suitable converter for the on-board charger, which will be able to decrease the charging cost by improving the power factor and the battery life span. This enhancement will be accomplished through the reduction of the charging current either at a very high or very low State of Charge (SOC). Isolated and non-isolated converter topologies were studied to identify the most suitable converter for the on-board charger that will be able to ameliorate the efficiency and the input power factor as well as control the charging current limits. A non-isolated buck converter with switched inductors is used for the power factor adjustment along with the current control approach to achieve a highly efficient on-board charger. Compared to the isolated converter with transformers, the non-isolated hybrid switched inductor buck converter has a wider current control range. MATLAB/Simulink output results were analyzed to validate the performance of the designed on-board charger with a non-isolated converter.

Keywords-EV charger; non-isolated converter; power factor correction; input and output current control; on-board charger

I. INTRODUCTION

Electric Vehicles (EVs) act as a solution for the elimination of fuel consumption. However, they are not yet extensively used. To achieve a wide spread utilization of EVs in the future, many challenges have to be overcome. One of the primary difficulties is the establishment of an extensive and efficient charging infrastructure. This includes both public charging stations and home charging solutions. The availability of charging stations, especially in regions with lower EV adoption rates, can significantly impact the convenience and feasibility of owning an electric vehicle. Charging an EV typically takes longer than refueling a traditional gasoline internal combustion vehicle. Fast charging technologies have improved this type of delay, but challenges still remain in terms of the charging speeds, especially for longer-range EVs. Balancing charging speed with battery longevity and safety is also a consideration. On-board chargers are an alternative solution for the charging

stations. Traditional on-board EV battery chargers as shown in Figure 1 are utilized to charge E batteries deploying isolated converters with built-in transformers, along with a voltage conversion isolation transformer employed to maintain safety. Traditional isolated chargers consist of two stage operations. In the first stage the rectifier used for Power Factor Correction (PFC) and in the second stage an isolated converter with coupled inductors is implemented for dc-to-dc voltage conversion [1-2]. The power factor rectifier regulates the input ac current while controlling the output dc voltage to maintain low total harmonic distortion and the sinusoidal shape as the input ac voltage [3]. In the isolated converter, input ac current ripples can be filtered by bulky coupled inductors. As the PFC rectifier already provides the constant dc output voltage, the dc-to-dc stage operates on isolated voltage with the help of a transformer [4-5]. Because of the two-stage function, isolated converters suffer from increased charging cost and less efficiency. The charger plays an important role in battery life

and vehicle performance. Discharging and charging are two unavoidable battery aging situations in EV applications. Irregular charging of the battery impacts the performance and battery life time, especially at very low and very high SOC. They deteriorate cell chemistry and reduce the life span of the EV battery [6-7]. Degradation of the battery can be identified with the State of Life (SOL). Internal resistance is a health indicator of a battery, and SOL is derived as:

$$SOL_{R_{based}} = \frac{R_{EOL}(SOC,T) - R_{ACT}(SOC,T)}{R_{EOL}(SOC,T) - R_{BOL}(SOC,T)} \times 100\% \quad (1)$$

where $R_{ACT}(SOC, T)$ is the actual internal resistance, $R_{BOL}(SOC, T)$ is the initial internal resistance of the battery, and $R_{EOL}(SOC, T)$ is the internal resistance of the battery at the end of its life at given SOC and temperature conditions. Equation (1) indicates that battery aging increases while charging the battery under high and low SOC conditions [8-11]. Conventional isolated converters with fixed turns ratio cannot control the charging current for a SOC lower than 20% or higher than 80%.

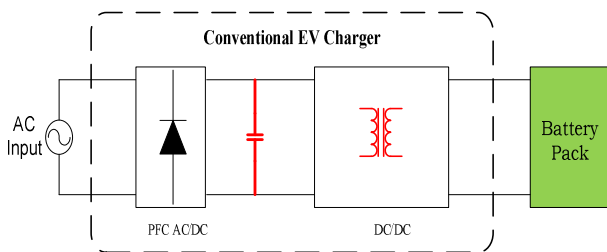


Fig. 1. Charger with isolated dc/dc converter.

To overcome the aforementioned challenges, the high power factor and a more efficient non-isolated converter with a switched inductor buck converter are proposed to be used in an on-board charger application. Figure 2 depicts the recommended isolated converter with an input EMI strain filter, a diode bridge rectifier, and a switched inductor buck converter [12-15]. The utilization of the switched inductor reduces bulky and large sized coupled inductors/transformers.

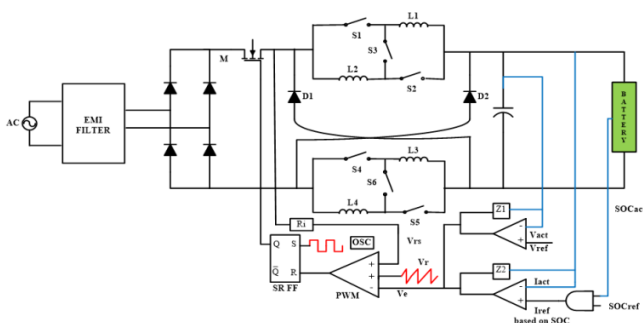


Fig. 2. The proposed non-isolated switched inductor buck converter.

II. SWITCHED INDUCTOR WITH HYBRID BUCK TOPOLOGY

A variation of the traditional buck topology, the switch inductor hybrid buck topology, has four inductors and two

diodes with one main switch (M) enabling the appropriate current direction flow, which divides the inductance in four parts ($L_1, L_2, L_3,$ and L_4). Authors in [1] used coupled inductors, but this work utilizes isolated inductors to reduce size [16]. The particular work switched the four inductors with six switches ($S_1 - S_6$) functioning just in DCM for increased efficiency as it permits ZCS and eliminates reverse recovery loss [17-18]. Additionally, lower inductance is possible with DCM operation, increasing total power density. Figure 3 illustrates the current direction flow through each operating stage of the switching inductor buck converter PFC design. The individual inductors were considered to be equal, i.e. $L_1 = L_2 = L_3 = L_4 = L$ [19-21]. When switch M is on, the four inductors will be in series and get charged up by the input source voltage through switching ON S_3 and S_6 as observed in Figure 3(a), whereas the output across filter capacitance is the output voltage fed to the battery. The average voltage across the inductor is $V_L = \frac{V_{in} - V_{out}}{4}$ [22]. When the switch M is OFF, the four inductors will be in parallel to load by switching ON $S_1, S_2, S_4,$ and S_5 as spotted in Figure 3(b).

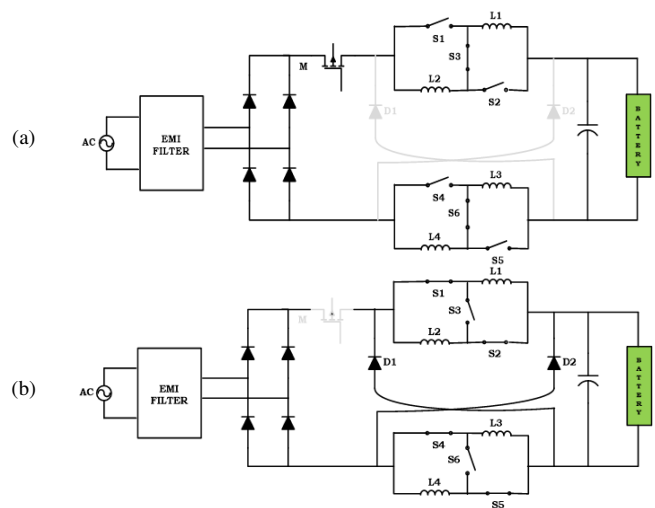


Fig. 3. Switched inductor buck converter current flow when (a) M, S_3 , and S_6 are on and (b) M is off and S_1, S_2, S_4, S_5 are on.

The Voltage across the inductor is $V_L = (-V_{out})$ [23]. To derive the voltage gain of the switched inductor hybrid buck converter, the average voltage across the inductor should be equal to zero over a complete period of operation, where the voltage gain is $V_{out}/V_{in} = D/(4-3D)$ [24]. The proposed PFC switched inductor hybrid buck converter is designed for EVs with 64 V battery. In order to achieve 64 V output voltage with the input voltage of 320 V, the duty ratio is set to $D = 0.5$.

A. Diode Losses in Switched Inductor Buck Converter

The switching frequency of the converter is thought to be 20 kHz with 40 kW output power rating. The inductor current ripple is regarded as 5% and the output voltage ripple as 2% with a maximum output current 0.625 kA. For the calculation of the inductance,

$$L = ((V_{in} - V_o) * D) / (F_s * \Delta i)$$

Considering $\Delta i=31.3A$, the calculated inductance is $L = 0.205 \text{ mH}$.

In the mode of discontinuous conduction (DCM), the inductor peak current depends on the duty ratio D [25-26], where $T=1/f_s$:

$$I_{L,pk} = \frac{V_{in}-V_{out}}{L} DT \quad (2)$$

where V_{in} is the input dc voltage, V_{out} is the output dc voltage, and T is the switching time. The duty ratio each time the inductor discharges current is:

$$D_{off} = \frac{1}{2} \left(\frac{V_{in}}{V_{out}} - 1 \right) D \quad (3)$$

which, in the switched inductor buck converter, is half of the traditional conventional buck converter's duty ratio. The overall power loss of the diode is given by:

$$P_d = \frac{L I_{L,pk}^2}{2 T v_{out}} V_d \quad (4)$$

where $L = 0.205 \text{ mH}$, $I_{L,pk} = 31.3 \text{ A}$, $T = 0.05 \text{ ms}$, and $V_{out} = 64 \text{ V}$. Equation (4) is similar for the standard isolated buck converter. The forward diode voltage $V_d = 0.8 \text{ V}$ for the switched inductor buck architecture will be marginally lower, because the same current value will be divided across the two diodes as $V_d/2$. As a result, the diode losses are lower than for the buck architecture. Table I summarizes the comparison.

TABLE I. COMPARISON OF DIODE POWER LOSSES

Total diode power	Isolated buck converter diode power loss	Switched inductor buck converter diode power loss
P_d	21.9665W	10.9832W

B. Conduction Losses in the Inductor

The conduction loss of the inductor depends on the rms current, which is computed in (2) with reference to the DCM peak current [27-28]. Consequently, the winding's inductor conduction losses are:

$$P_L = \frac{T^2 D^3}{3L^2} (V_{in} - V_{out})^2 \frac{1}{2} \left(1 + \frac{V_{in}}{V_{out}} \right) X_L \quad (5)$$

where X_L is the total series reactance of the inductor, equal to $2\pi fL$, $f = 1/T$ is the frequency of the ac input passing through the inductor given as 50 Hz . The calculated X_L is 64.75Ω . In the switched inductor buck converter, the reactance is equally distributed throughout the four inductors ($R_L/4$). To observe the difference of the two different topologies, the traditional standard buck conduction loss $P_{L,conv}$ is divided with (5), which yields:

$$\frac{P_{L,SLH}}{P_{L,conv}} = \frac{4}{1 + \frac{V_{out}}{V_{in}}} \quad (6)$$

The converter voltage gain ranges between $0 < \frac{V_{out}}{V_{in}} < 1$, the conduction loss of the inductor in the standard buck is 4 times that of the switched inductor hybrid converter. Table II summarizes the comparison.

TABLE II. COMPARISON OF INDUCTOR POWER LOSSES

Total inductive power	Isolated buck converter inductor power loss	Switched inductor buck converter inductor power loss
P_L	31.5W	7.8W

III. POWER FACTOR CORRECTION TOPOLOGY

The proposed PFC switched inductor hybrid buck converter employs the Current Control Mode (CCM), which is a straight forward implementation and allows high power factor without any sampling of the input source voltage [29-31]. It is not necessary to use input voltage sensing because the slope of the inductor current is proportional to the source voltage. CCM works and monitors each switching cycle by sensing, detecting, and clamping the inductor current. The direction of the current flowing through the inductor during every switching cycle is portrayed in Figure 4. The inductor current is measured and compared to a reference value minus a slope adjustment ramp. The switch is turned off and clamps the current, if the detected current reaches the ramp value. To further enhance the PF in DCM2 (Discontinuous Conduction Mode 2), a scalar normalizing coefficient is additionally used to the value of the slope compensation ramp [32]. If the current cannot reach the value of the compensation ramp, turning off the switch is accomplished by operating it, when the duty ratio reaches its maximum value while operating in DCM1 (Discontinuous Conduction Mode 1).

A. Discontinuous Conduction Mode 1

In DCM1, the topology of the switching duty ratio is fixed as D_{max} , so the mean input current expressed as a function of the angle θ is given as:

$$i_{i,DCM1}(\theta) = D^2 \max \frac{V_{in} |\sin \theta| - V_{out}}{2L_{onfs}} \quad (7)$$

The input line voltage amplitude V_{in} is 320 V , and the switching frequency f_s is 20 kHz . The input current is proportional to the difference between the input voltage and the output voltage. The current only flows when the input voltage exceeds the output voltage. For $D = 0.5$, it is estimated that $V_{out} = 64 \text{ V}$. Without the DCM1, the phase of the current loses its sinusoidal shape. Current conducts whenever, $V_{im} (320 \text{ V}) > V_{out} (64 \text{ V})$. To avoid the PF problem, the switching operation is performed as shown in Figure 4.

B. Discontinuous Conduction Mode 2

In DCM2, the duty ratio is calculated based on ramp and the converter operation condition, so that the input current is:

$$i_{i,DCM2}(\theta) = \frac{I_{ref}^2 L_{onfs}}{2} \frac{V_{im} |\sin \theta| - V_{out}}{[V_{im} |\sin \theta| - (1+2k_s)V_{out}]^2} \quad (8)$$

The maximum switching duty ratio is configured as 0.8 . Various k_s values from 0.5 to 1 are considered and mathematically analyzed using MATLAB. In general $k_s = 1$ demonstrates the ideal PF and THD effectiveness. Half a cycle of the input voltage with half wave symmetry are deployed. In the switched inductor hybrid buck converter, the reference voltage should always be less than the peak of the input voltage. The converter runs in DCM1 mode between t_0 and t_1 because the reference voltage is greater than the input voltage.

In $[t_1, t_2]$, the converter runs in DCM2 mode, which means that the current conducts with switching even if $V_{im} < V_{out}$, and this is repeated for $[t_2 - t_3]$. The controller handles the switchover from CCM to DCM mode and vice versa, ensuring seamless transitions every quarter of a cycle.

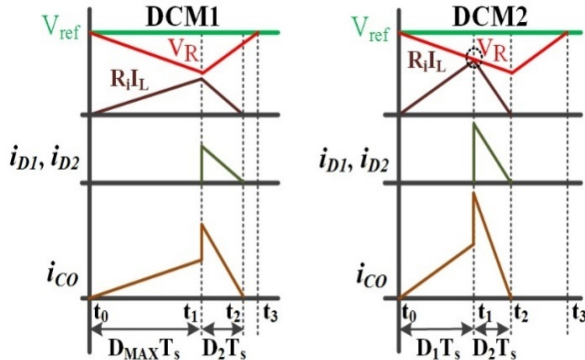


Fig. 4. Switched inductor buck converter current control mode.

The control algorithm with respect to the input voltage, output voltage, and SOC for an isolated hybrid converter, controls the input, and adjusts dc-link voltages. The input values are calibrated to k_p, k_i gains, giving rise to minimum tracking error when employing PI controllers. Two PI controllers are used to regulate the ac components (i_d, i_q) as well as the voltage across the dc connection. The internal current regulation loop controls the current, whereas the outside voltage control loop controls the dc link voltage. To determine a PF, the reference value for the q axis current (i_q) is adjusted to 0. Initially, all switches are turned off, and the bulk capacitor is precharged with the anti parallel diodes and an inrush limiting resistor. The inner current loop creates a duty ratio, which ensures that an average inductor current is traced correctly. The outer part of the voltage looping process adjusts the voltage at the dc link by adjusting its input current amplitude. The PI controller is utilized to modify the voltage that controls the loop's gain. The frequency of the crossover should be set to ensure that the dc-link voltage ripple at the double line's frequency is minimal. Finally, feed-forward control logic is employed to calculate the duty ratio. The in-phase current source within the internal control loop results in a PF that is close to 1 and lower input current THD.

IV. CHARGING CURRENT CONTROL TOPOLOGY

The on-board chargers are capable of charging the battery with a household grid load. However, the car's capacity is limited to 240-300 Wh/mile. Fast-charging alternatives are required for cars that have to make additional journeys outside their normal route. Chargers having a power output below 3.3 kW with 1-phase are referred to as slower chargers as well as output 1 chargers with a 120 V outlet. They can be included into the vehicle's engine (on-board) or used for a wall-mounted charging outlet. Level 1 chargers have extended charging times: 5-12 h for 2.1 kW hybrid EV battery capacity of 4-16 kWh and 9-256 h for 2.1 kW EV battery capacity of 15-49 kWh. Level 2 chargers are capable of charging both 3-phase and 1-phase EV batteries up to 21.5 kW [20]. Level 2 chargers,

like Level 1 chargers, can be integrated within the car or located outside as specialized EVSE. Level-2 chargers provide three charging time scenarios: 2-5 h for 5 kW with hybrid EV battery capacity of 6-16 kWh, 1-2 h for 18.9 kW with EV capacity of 2-40 kWh and 3-7 h for 9 kW with EV battery capacity of 17-29 kWh [20]. Fast charging converters enable the creation of Level-2 chargers.

The advancement of fast-charging converters encourages the shift to the utilization of EVs. Charging strategies are critical in determining the general effectiveness of the EVs. Fast-charging converters create hurdles for the EV technology, including power loss and battery temperature issues. The primary cause of the capacity loss with cycle count is their stabilization over the usage time. The solid electrolyte interface's passivation layer growth and consolidation create a more stable interface that lowers the rate of corrosion. But again, oxidation is the primary issue of the positive electrode during the storage phase. Charging time at higher SOC is the dominant factor which affects the oxidation properties, causing the power capability to decline and the internal resistance to rise [33-35]. An efficient charger for battery packs charges the battery pack securely and efficiently, while also meeting international standards like IEEE 1547. Conventional charging techniques attempt to charge the battery with the non-linear characteristic. Hence, the proposed isolated hybrid buck converter charges the battery with 3 different protocols: (1) Linearly Declining Current (LDC), (2) pulse charge, and (3) current limiting. The SOC of the battery is determined by the Coulomb counting technique:

$$SOC(t) = SOC(t-1) + (\int i(t)dt) / \text{nominal capacity} \quad (9)$$

From (1), the age of the battery is divided into 3 stages. In the first stage, the LDC charging strategy is applied. This reduces charging current 0.2 C times based on the battery pack SOC from 0% to 20%. For LDC charging, the initial SOC is going to be taken into consideration. In the second stage, using pulse current, a battery is able to be charged quickly and efficiently with a charging rate of 4 C, if the SOC is between 20% and 80%. The disadvantage is that quick-charging might harm the battery's health when the SOC exceeds 80%. In the third stage, if the SOC is more than 80%, charging currents are applied. In this stage, the battery charging current should be reduced by 50%. Control of the current based on SOC has been performed with the PI loop. With the proposed on-board charger with the isolated hybrid buck converter, the SOL of the battery was improved by 1.3 times. With the current limits applied while in high SOC, the temperature of the battery will be stable.

TABLE III. SWITCHED INDUCTOR HYBRID BUCK CONVERTER COMPONENT PARAMETERS FOR SIMULATION ANALYSIS

Component parameters	Value
Input DC Voltage	320 V
Output DC Voltage	64 V
Switching frequency	20 KHz
Switched inductor hybrid buck inductor	0.205 mH
Switched inductor hybrid buck capacitor	3.91 mF

V. SIMULATION RESULTS

In Figure 5, the isolated buck converter engages a transformer with turns ratio of 3:1, resulting in more losses. Figure 6(a) displays the input ac voltage of 320 V rms given to the isolated converter and the output voltage of 62.3 V dc. The input ac is not sinusoidal. Figure 6(b) exhibits the THD of the input AC current with the fundamental frequency of 50Hz.

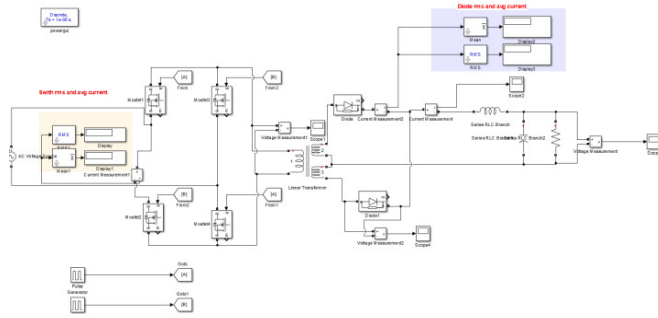


Fig. 5. Simulated isolated converter designed in Matlab/Simulink.

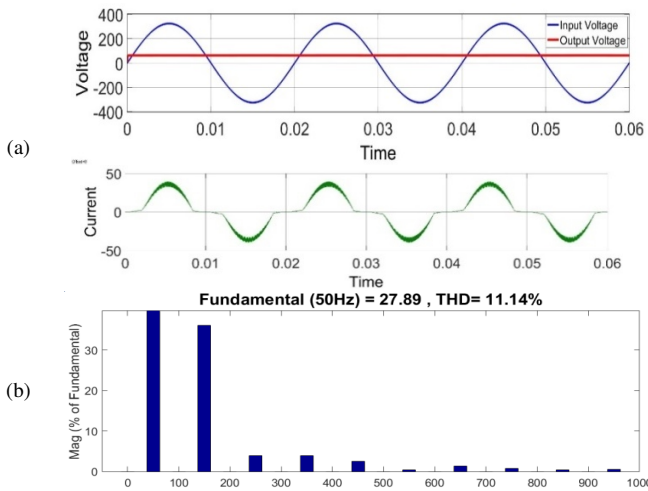


Fig. 6. (a) Isolated converter input voltage, input current, and output voltage, (b) isolated converter THD of input current.

Figure 7(a) presents the design of the proposed switched inductor hybrid buck converter. In Figure 7(b), the control algorithm of the switched inductor hybrid buck non-isolated converter is designed. The control algorithm takes reference of the shape of the input voltage, the input current, and the charging current limits based on SOC.

Figure 8(a) shows the input AC voltage of 320 V rms given to the switched inductor hybrid buck non-isolated converter and the output voltage of 64V DC. The input AC current is sinusoidal. Figure 8(b) depicts the THD of the input AC current with the fundamental frequency of 50 Hz. The THD of the switched inductor hybrid buck non-isolated converter is less than 5.

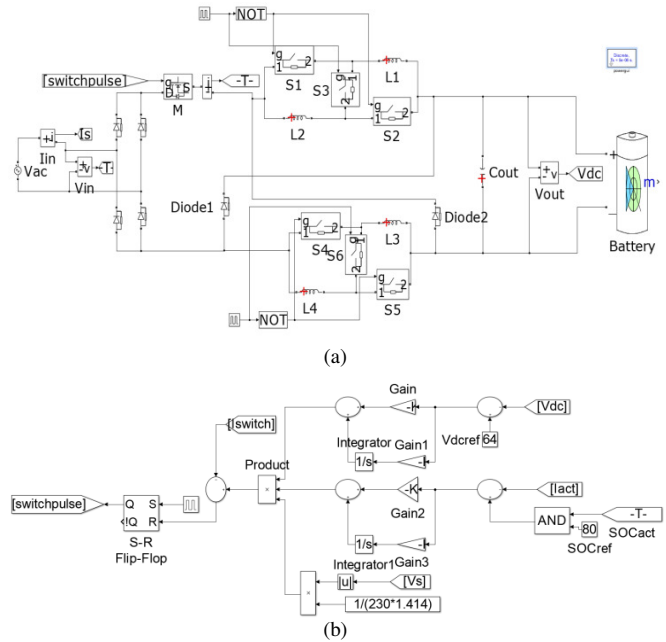


Fig. 7. (a) Simulated switched inductor hybrid buck non-isolated converter, (b) simulated PI-based controlled logic.

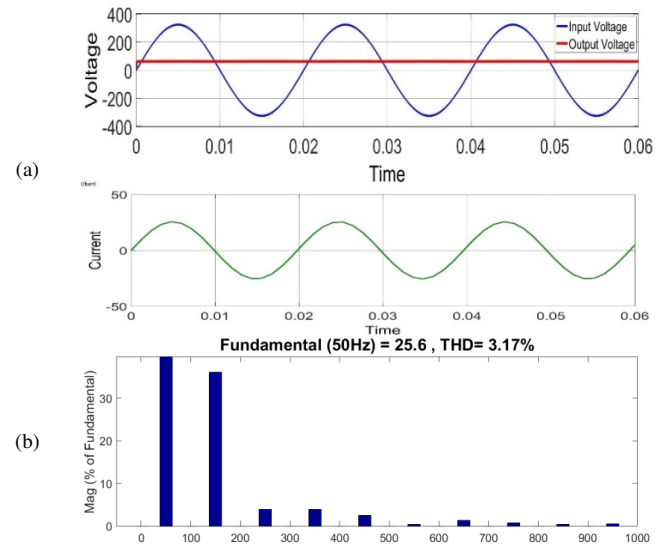


Fig. 8. (a) Switched inductor hybrid buck non-isolated converter input voltage, input current, and output voltage, (b) switched inductor hybrid buck non-isolated converter THD of the input current.

In Figure 9(a), it can be noticed that changing SOC from 70% to 80% took around 350 s. After reaching 80%, the charging current limits were applied, the charging current was reduced by 50%, and hence the rate of charging slowed down. With the charging current limits applied at higher SOC values, the battery temperature did not increase, so the battery life of the improved. The activation of the current limits is spotted in Figure 9(b). Figure 10 illustrates the calculated losses of the isolated and switched inductor hybrid buck non-isolated converters.

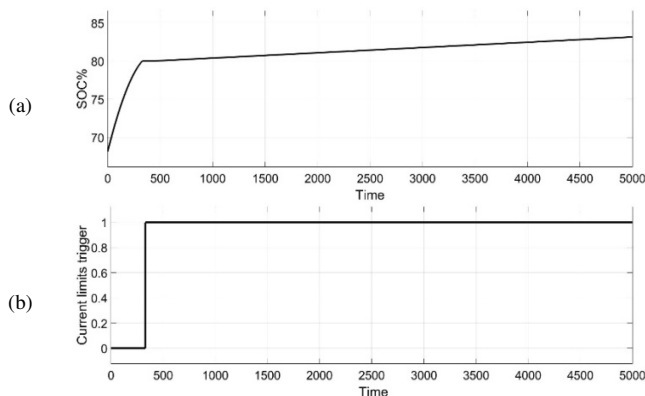


Fig. 9. (a) Battery SOC of the switched inductor hybrid buck non-isolated converter, (b) current limit activation from the control logic.

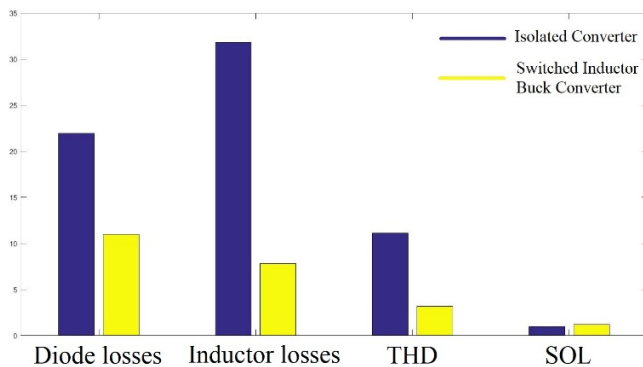


Fig. 10. Calculated losses of the isolated and the switched inductor hybrid buck non-isolated converter.

As demonstrated in Table I, the diode losses of the isolated buck converter are 21.96 W and the diode losses of the switched inductor buck converter are 10.98 W. The inductor losses (Table II) of the isolated buck converter are 31.96 W and of the switched inductor buck converter are 7.8 W. From Figure 6(b) and Figure 8(b), it is noted that the THD of the switched inductor buck converter is less. With the charging limits applied, the SOL of the battery is improved by 1.2 times, proving that the switched inductor hybrid buck converter is efficient for on-board chargers.

VI. CONCLUSION

A non-Isolated buck PFC converter suitable for application to electric vehicle chargers, with switched inductor and using controlled current mode was introduced, analyzed, and verified with simulations. The method for implementing the current controlled mode and selecting the k_s ramp value was presented. Mathematical analysis revealed that the switched inductor buck converter topology has overall losses above all the inductor conduction losses compared to the isolated buck converter. The 60 W simulated model was tested, and the efficiency of the non-isolated buck converters was always larger than that of the traditional isolated buck converters. These findings support the effectiveness and high efficiency of the non-isolated switching

inductor buck converter as an electric vehicle charger application.

FUTURE WORK

This work can be also extended to other types of chargers, for the correction of the power factor in order for the EVs to have better efficiency and battery life.

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