

Intertied AC-DC Hybrid System Power Sharing Through Intelligent Droop Controller

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Abstract—The result of DG clustering is the hybrid power system while further clustering forms the intertied hybrid power system. Interfacing of intertied hybrid power system requires an interlinking converter with a legitimate power administration and control system. In contrast to individual hybrid power system (HPS), power administration of the intertied hybrid system is more complex. Autonomous droop strategy is appropriate for the intertied hybrid system where communication links are not possible. This paper proposes a new topology for control in intertied hybrid system where two hybrid power systems are connected to each other through interlink power converter. Evaluated frequencies in different HPSs can be diverse. In order to manage power flow a power management strategy with consideration characteristics of common bus, a $P_{DC}-v_{DC}^2$ method is proposed, and compared with conventional droop, to realize power sharing among HPS. The practicability of the proposed power sharing method is realized in MATLAB/Simulink platform.

Keywords—intertied hybrid system; power sharing; intelligent controller

I. INTRODUCTION

Concerns about natural outflow from centralized power plants have extended excitement for DG establishment, despite the fact that DG infiltration in power structures is limited due to specific reasons like reliability necessities [1]. Hybrid power Systems (HPSs) are required to work both in grid connected power systems and standalone modes to fortify dependability and power quality [2]. Grid accountability to maintain the system voltage and frequency with appropriate sharing of power in autonomous system is desirable [3-6]. The intertied hybrid system is a focus of research due to its high reliability and flexibility with integration of renewable energy sources. In this paper, the intertied hybrid system composed of different voltage and different frequency HPS with either DC or AC source is considered. Hence the intertied hybrid system can utilize the important characteristics of both DC and AC HPS. In contrast to individual HPS, power management of the intertied HPS is more intricate because of interfacing AC and DC HPS. Hence the design of a power sharing controller for exploiting the features of intertied HPS is a major challenge. For diverse applications, the configuration of intertied HPS can be distinctive. The conventional intertied HPS normally

includes one AC HPS and one DC HPS. Concentrating on this issue this paper proposes a new control method for intertied HPS with two HPSs of different frequency. Some fast communication methods by using master-slave techniques is discussed in [7, 8], but due to requirement of communication links the system is not more reliable. While designing a controller for a VSC based hybrid system without communication link some issues have to be considered and conventional techniques might be inappropriate. In [9, 10], authors discussed conventional droop control, where enhanced DC current sharing is obtained with suppression of DC voltage deviations by low bandwidth communication signals but it excludes time delay effects. Moreover, distributed secondary control in [11] overcomes the shortcoming of the conventional droop. Different power management strategies of hybrid microgrids are discussed in [12]. From the perspective of power balance $v_{dc}^2-p_{dc}$ droop control is developed in [13]. Power sharing among intertied HPS with artificial intelligence is proposed in [14] to enhance the operating battery lifetime. To overcome the limitations of previously mentioned methods an intertied HPS with multiple HPS is considered in this paper. Proposed controller adopts decentralized controller which enhances the reliability of the system. The attractive feature of the control method is the linear relation between v_{dc}^2 and P_{dc} . The proposed control assures power sharing in coordination with voltage and frequency support. Also the control scheme is appropriate for different HPS capacities. The effectiveness of the proposed power sharing control is verified by SPS toolbox.

II. INTERTIED HYBRID SYSTEM TOPOLOGY

The proposed scheme for intertied HPS is shown in Figure 1. The intertied HPS is composed of multiple AC HPS of different rated frequencies and different rated DC HPS. Intertied HPS are strictly free to have their own voltage and frequencies that better match with their sources and load. Some countries have rated frequency of 50Hz, others 60Hz, 400Hz frequency is found in aircraft supply networks. Therefore different conditioned HPS interlinking is necessary for appropriate power sharing and load adaptability. The higher system inertia of the intertied HPS keeps the system stable under perturbation, while the feature of lower reserve requirement reduces the startup capital cost which includes the cost of inserting power converters. Addition of power

converters is therefore a universal approach more likely to draw researcher’s interest. Some of already existing HPSs include standalone power networks for electric ships, electric aircrafts and electric vehicles with their own preferred voltages and frequencies even though they are usually compactly small. The thought of clustering and interlinking has, in fact, been recently promoted by research, concentrating on the division of a local area network into subzones/ clusters of either the same or different voltage levels. The objective there is to enhance system reliability, security and efficiency by minimizing transmission and distribution losses. MPPT can be applied for the DGs having intermittent nature. Different IPCs are employed to interconnect different HPSs according to rating. Interlinking converters play an important role in providing bidirectional energy transfer between HPSs. The intertied HPSs can be connected to the AC utility mains through an intelligent switch.

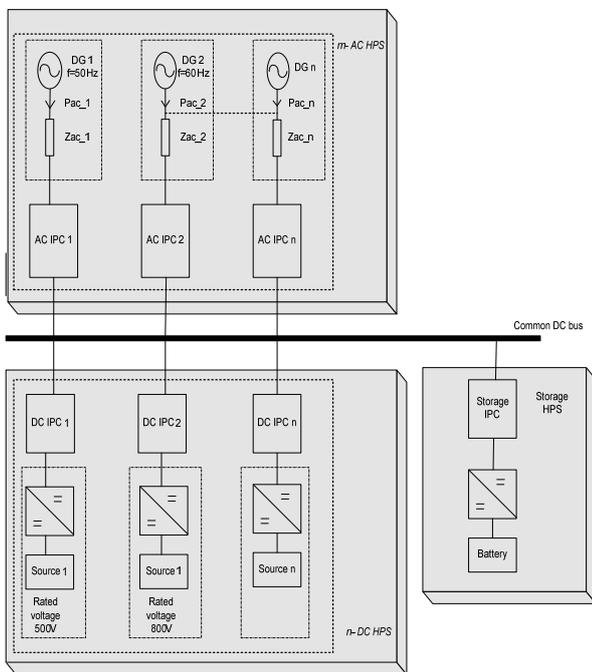


Fig. 1. Intertied hybrid system topology

A standalone system should be capable of fulfilling the load demand. The sources will deliver the power depending upon the information received through explicit communication links without overstressing themselves. The problems of cost increase and single point failure can be resolved by droop control, where adjustment of voltage magnitude and frequency is to be sensed by each HPS. The intertied HPS considered in this paper is in standalone mode, however the controlling of standalone system is more complex when compared to grid connected system.

III. AUTONOMOUS CONTROL FOR INTERTIED HYBRID SYSTEM

Droop scheme has been extensively used for power sharing in conventional power generation which is now extended to

intertied HPS. The storage system satisfies the load demand under the power supply failure. The function of the storage system is to sustain the common DC link voltage with proper power sharing. The battery is connected to common bus through a DC-DC boost converter. Since the intertied HPS constitutes many HPSs including storage system, they need to be connected to common bus with IPCs. The main role of IPC is to manage the power sharing and maintain constant voltage. When power failure occurs and storage system is providing the power to loads the relation between i_L and v_{dc} is not linear:

$$i_L \propto \frac{dv_d}{dt} \tag{1}$$

Due to nonlinear relation between current and voltage conventional droop control will not provide satisfactory performance. To look into power balance scenario the relation between P_{dc} and v_{dc}^2 is linear which motivates the application of a new droop strategy for improved performance. The control strategy involves inner and outer loops, where the controlling of v_{dc}^2 is achieved by inner loop control by tracking the reference generation by outer loop. The power sharing is achieved by outer loop control by $P_{dc}-v_{dc}^2$ droop. The inner loop static error elimination is done by PI controller followed by proportional controller to advance the system damping and reliability. The control law for $P_{dc}-v_{dc}^2$ droop is expressed as

$$v_{dc}^{ref^2} = v_{cb}^{*2} - r(P_{dc} - P_{dc}^*) \tag{2}$$

where, v_{cb} , v_{dc}^{ref} , P_{dc} and P_{dc}^* are the droop coefficient, common bus voltage, reference output voltage, DC power and reference DC power respectively. Figure 2 shows the simplified circuit of the proposed droop control where v_{cb} is the common bus voltage and R_{line} is the line resistance. DC power can be calculated as

$$P_{dc} = \frac{v_{cb}^{*2} + rP_{dc}^* - v_{dc}v_{cb}}{R_{line} + r} \tag{3}$$

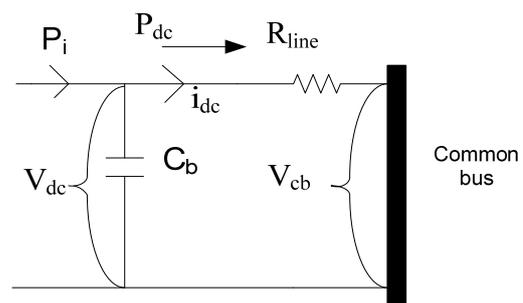


Fig. 2. Circuit simplification for proposed droop control

To simplify the equation assume $G = v_{cb}^{*2} + rP_{dc}^*$, and $r \gg R_{line}$ then (3) can be rewritten as

$$P_{dc} = \frac{G - v_{dc}v_{cb}}{r} \tag{4}$$

$$\frac{rP_{dc}}{v_{cb}} = G - v_{dc} \tag{5}$$

After solving the above equations P_{dc} can be written as

$$P_{dc} = \frac{G - v_{cb}^2}{r} \tag{6}$$

The equation implies that P_{dc} is inversely proportional to r and the matching of P_{dc} and r is according to storage capacity. Droop control method for HPS₁ and HPS₂ is based upon the conventional method. In (7)-(10) x is DG unit number, f is the frequency, V is terminal voltage, P is active power and Q is reactive power generation respectively.

$$f_x = f_{max} - m_x P_x \tag{7}$$

$$m_x = \frac{f_{max} - f_{min}}{P_{x,max}} \tag{8}$$

$$v_x = v_{max} - \eta_x P_x \tag{9}$$

$$\eta_x = \frac{v_{max} - v_{min}}{Q_{x,max}} \tag{10}$$

where m_x and η_x are active and reactive droop coefficients. In this paper, four HPSs are considered intertied by interlinking converters. Droop control has the feature of active power flow from under-loaded to overloaded HPS. Droop control works under three conditions: under loading, over loading and heavy overloading. Conventional droop control for the proposed system is shown in Figure 3 where interlinking converters are implemented with two six switch converters. Each interlinking converter's power is equal and opposite to the others'. Outer loop consists of two PI controllers for active and reactive power errors. The two HPSs of different frequencies are connected to the common DC bus. Control strategy has two loops, inner and outer loop. The inner loop to tracks the reference to be generated by the outer loop. The reference value generated by the outer loop can be expressed as

$$f^{ref} = (f^* + \delta f) + m(P^* - P) \tag{11}$$

$$v^{ref} = V^* + n(Q^* - Q) \tag{12}$$

Here f^{ref} and V^{ref} , f^* , V^* , P^* , Q^* , P , Q , m and n are the reference output frequency and voltage, rated frequency, rated voltage, rated active power, rated reactive power, actual active and reactive power and droop coefficients respectively. δf is the change in the frequency due to change in load among different HPSs.

For interconnection of DC HPSs, high voltage side is connected to common bus. The control procedure with two loops has $i_{dc}-v_{dc}$ characteristics to generate reference signal. The outer loop is used to track the reference signal. The control method follows the relation as

$$v_{dc}^{ref} = (v_{dc}^* + \delta v) - r i_{dc} \tag{13}$$

where V_{dc}^{ref} , V_{dc}^* , r , i_{dc} , δV are reference DC voltage, rated DC voltage, droop coefficient, actual output DC current and change in the voltage corresponding to change in the load among different HPS.

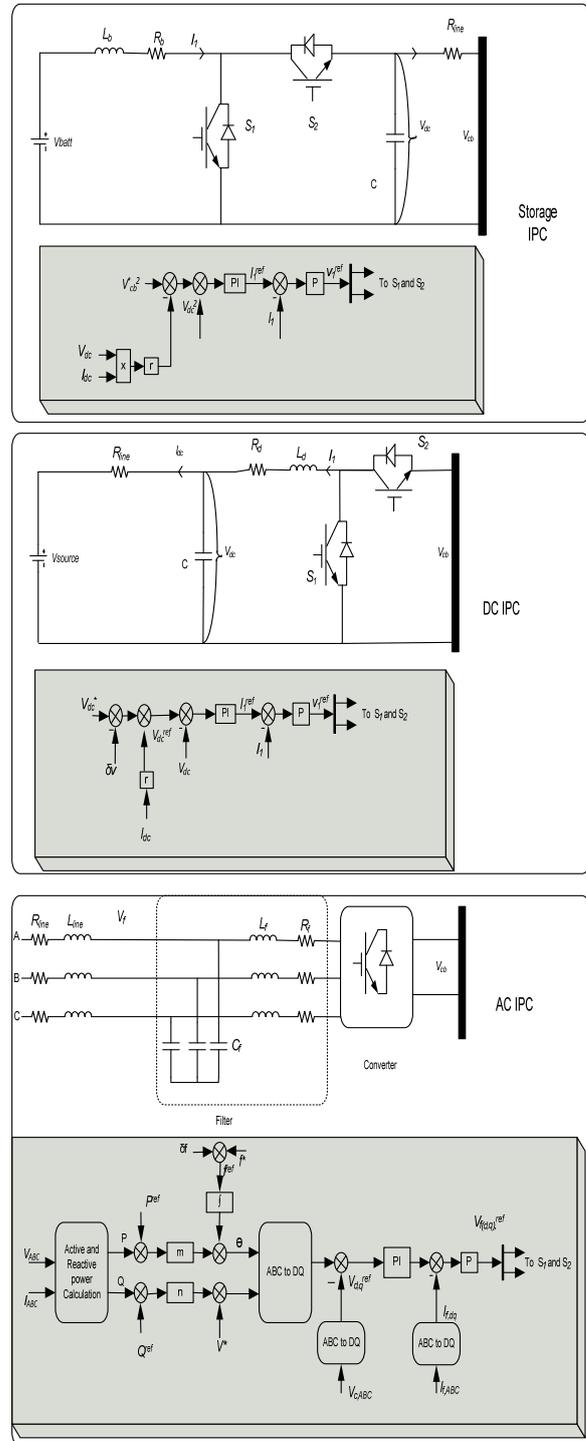


Fig. 3. Controller for intertied hybrid system

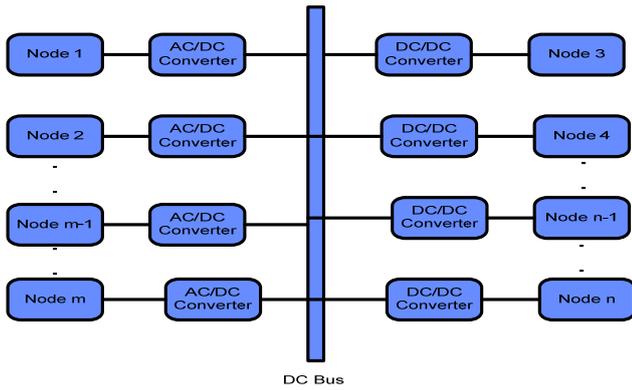


Fig. 4. Interconnection of IPC in intertied hybrid system

IV. POWERSHARING AMONG DIFFERENT HPSs

Section III focused on the control method for an individual HPS. Power interaction among different HPSs in a intertied system is more complex and is analyzed in this section. The coordinated control among individual HPSs ensures power fluctuations divided in all HPSs whether DC or AC. Enhanced performance can be achieved by supporting all HPSs. All HPSs are droop controlled, which implies changes in frequency and DC voltage according to change in load demand. The change in DC voltage for DC HPS and frequency for AC HPS represents power changes inside the DC or AC HPS. AC and DC IPCs absorb power from or release power to the common bus, which represents changes in the whole intertied hybrid system. Additionally frequency relates to power of AC loads and DC voltage relates to power of DC loads. Above mentioned changes in frequency, DC voltage and common bus voltage require coordinated control for AC IPC and DC IPC. The control law can be expressed as

$$\delta f_m = \left(k_{p,m} + \frac{k_{i,m}}{s} \right) \left[\frac{(v_{cb} - v_{cb}^*)}{v_{cb}^{max} - v_{cb}^{min}} - \alpha_m \frac{f_m - f_m^*}{f_m^{max} - f_m^{min}} \right] \quad (14)$$

$$\delta v_n = \left(k_{p,n} + \frac{k_{i,n}}{s} \right) \left[\frac{(v_{cb} - v_{cb}^*)}{v_{cb}^{max} - v_{cb}^{min}} - \alpha_n \frac{v_{dc,n} - v_{dc,n}^*}{v_{dc,n}^{max} - v_{dc,n}^{min}} \right] \quad (15)$$

where δf_m and δv_k are the coordinated control signal for IPC of m -th AC HPS and IPC of n -th DC HPS respectively. Proportional and integral constants for respective AC or DC HPS are given by k_p and k_i . α_m and α_n are the correction coefficients in consideration to capacity of m -th and n -th AC and DC HPS respectively. f_m , f_m^* , f_m^{max} and f_m^{min} are actual frequency, rated frequency, maximum and minimum allowable range of frequency, v_{cb} , v_{cb}^* , v_{cb}^{max} and v_{cb}^{min} are actual common bus voltage, rated common bus voltage, maximum and minimum allowable range of common bus voltage respectively.

Considering the capacity of the AC and DC HPS, if one HPS has high capacity compared to other HPSs then the weak HPS contribution to the system is less and strong HPS should absorb or release power. In view of the above coordination the correction coefficient is proposed. The correction coefficient of p -th HPS can be calculated as

$$\alpha_p = \left(\frac{H_p^{total}}{H_p^c} \right)^{-1} \frac{H_p^c}{H_p^{total}} \quad (16)$$

Where H_p^{total} and H_p^c are total and critical load capacity of p -th HPS and H^{total} is the total capacity of intertied HPS. It is clearly depicted from (16) that, for small capacity and high amount of critical loads, correction coefficient is large and subsequent changes in DC voltage and frequency are small. Synchronization between change in voltage and change in frequency proves the efficacy of the proposed method. Under steady state condition the relation between voltage and frequency considering capacity of the system is given by (17):

$$\frac{(v_{cb} - v_{cb}^*)}{v_{cb}^{max} - v_{cb}^{min}} = \alpha_p \frac{f_p - f_p^*}{f_p^{max} - f_p^{min}} \quad (17)$$

Figure 5 shows the corrected relative changes in AC frequency and DC voltage for the proposed control algorithm. The feedback variables are positioned at IPCs to share the coordinated power and can be realized to improve the reliability of the system.

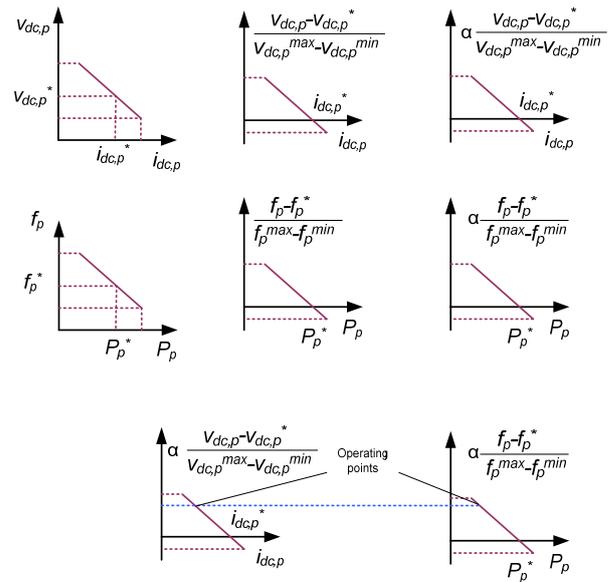


Fig. 5. Corrections for Coordinated Power Control

V. RESULTS AND DISCUSSION

The simulation was performed in MATLAB/Simulink using building blocks from SPS toolbox. The rated AC voltage of the HPS₁ and HPS₂ is 311V with frequency 60Hz and 50Hz respectively. The permissible variation range of the frequency is ± 0.5 Hz and ± 0.6 Hz respectively. The rated DC voltage of the HPS₃ and HPS₄ is 375 V and 380V respectively. The allowable variation range of the DC voltage is ± 25 V. The rated load power of HPS₁, HPS₂, HPS₃ and HPS₃ are 180KW, 130KW, 80KW and 70KW respectively. The storage HPS maintains the DC voltage of the common bus, whose rated voltage is 1000V. The rated voltage of the battery is 370V. The

allowable variation range of the common bus voltage is $\pm 50V$. The parameters of storages, IPC_1 , IPC_2 , IPC_3 and IPC_4 and are introduced in detail in Table I.

TABLE I. CONTROLLER PARAMETERS

Controller	HPS and IPC controller	
	Controlling Parameter	Value
Battery	Battery voltage	600V
Common Bus	Rated Common bus voltage	1000V
Voltage controller	Storage K_p	$0.0007A/v^2$
	Storage K_i	$0.07A/v^2-s$
	IPC 3 & 4 K_p	$0.6A/v^2$
	IPC 3 & 4 K_i	$50A/v^2-s$
	IPC 1 & 2 K_p	$0.8A/v$
IPC 1 & 2 K_i	$100A/v-s$	
Current Controller	Storage K_p	$8v/A$
	IPC 3 & 4 K_p	$3v/A$
Coordinated Power control	Coefficient	$\alpha_1=4, \alpha_2=0.75$
	K_p	$0.03Hz$
	K_i	$3Hz/s$
	Frequency	$f_1=60Hz, f_2=50Hz$

To verify the proposed control, the dynamics of the intertied hybrid system operation states of the HPS_1 are changed while other HPS states were kept stable. The system works in islanded mode under 2 states where state-I and state-II represents the rated state and variable state respectively. In state-II, load increases from 130kW to 180kW under the rated AC frequency and voltage. Figure 6 shows the dynamics of the intertied hybrid system with the proposed control with changes in operation states of the HPS_1 . Figure 6(a) shows the load power where load at HPS_1 is suddenly increased from 180Kw to 260KW. The AC frequencies are changed in coordination, which shows that AC and DC HPSs support each other. In state II, the load power of HPS_1 increases, consequently AC frequency decreases. Due to HPS_2 lower capacity relative changes of f_1 are smaller compared to the relative changes of f_2 . From Figure 6(b), it is observed that the power coordination among HPSs is well achieved. Increase in the load power of HPS_1 is the overloading state of HPS_1 . The other HPS supports the power consumption by decreasing the DC voltages and AC frequencies. To match with electrical demand at HPS_1 , all other HPS supports and IPC are adjusting the power output according to correction coefficient α . Since the correction coefficient α is different for different HPSs so the relative changes in AC frequencies and DC voltages will be different as shown in Figure 6(c).

Figure 7(a) shows the load power where load at HPS_1 is suddenly decreased from 240Kw to 170KW but load on other HPS remains constant. To cope up with the decrease in load all other IPCs will coordinate and decrease the source power according to total load demand (Figure 7(b)). The incremental source power is based upon the correction coefficient. Since the correction coefficient of all HPSs is different and based upon HPS capacity, each HPS will change power accordingly. The decrease in load results in increase in frequency and due to coordinated control alone HPS_1 will not bear the major changes, the other HPS will substantially increase the frequency in specified limit which reduces the overburden on HPS_1 .

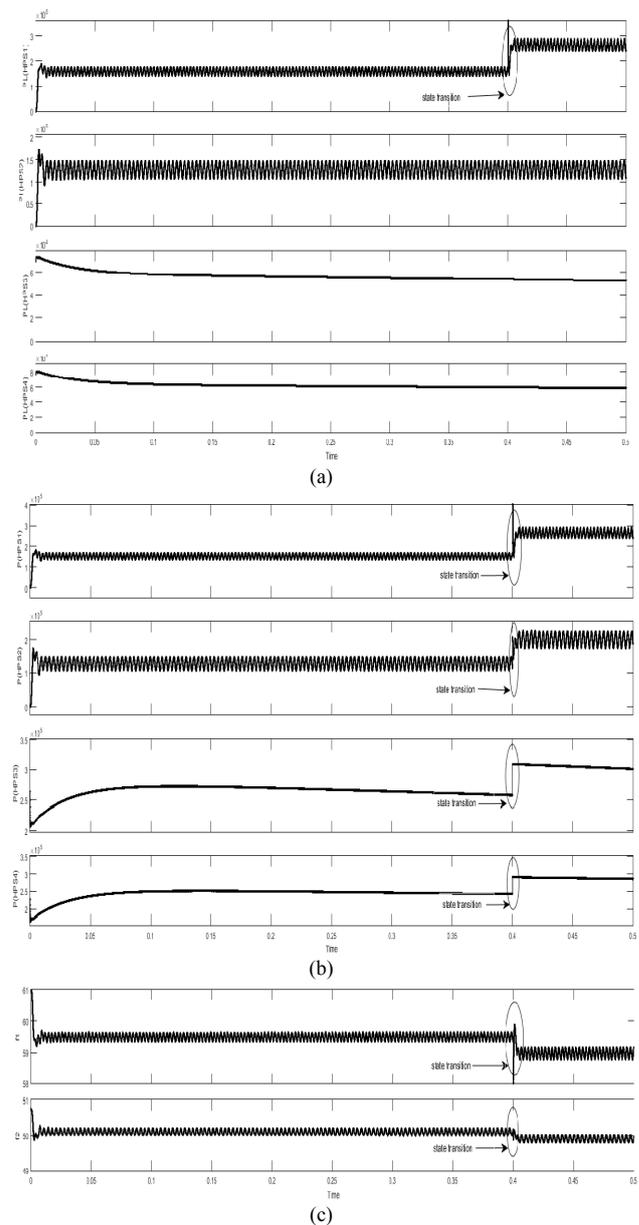


Fig. 6. Simulation results of intertied hybrid system with coordinated control (a) Load power for increase in load on HPS_1 (b) Source power for increase in load on HPS_1 (c) Frequency for increase in load on HPS_1

To additionally verify the viability of the proposed power management method, the dynamics of the intertied hybrid system are tested with changes in operating conditions of the DC HPS_3 . In Figure 8 the outcomes are presented. As Figure 8 shows, there are two states. The entire system operates in the stand alone mode with state I as rated state and state II as variable state. In stateII, load on HPS_3 increases from 70kW to 130kW at 0.2 sec under the rated DC voltage. The operation states of the other HPS in intertied hybrid are not changed. Figure 8(a) demonstrates the change in load power of HPS_3 . As shown in Figure 8(b), due to changes in load power of HPS_3 , DC voltages of other HPS changes in coordination, which implies that all HPSs in intertied hybrid system can support each other.

State I represents increase in load power of HPS₃, the coordination control will match the load demand with all the HPS connected in intertied hybrid system as shown in Figure 8(c).

the load on HPS₃ decreases from 70kW to 130kW at 0.2sec under the rated DC voltage. The operating conditions of all HPS connected to intertied hybrid system are unchanged. Figure 9(a) shows the change in load power of HPS₃ at 0.2sec. from 60KW to 30KW. Decrease in load at HPS₃ will result in increase in DC voltage and coordinated control increases the DC voltage of other HPS proportionate to the correction coefficient. In state I when the load power of HPS decreases, due to the coordination control the source power of all HPS will decrease accordingly as shown in Figure 9(c).

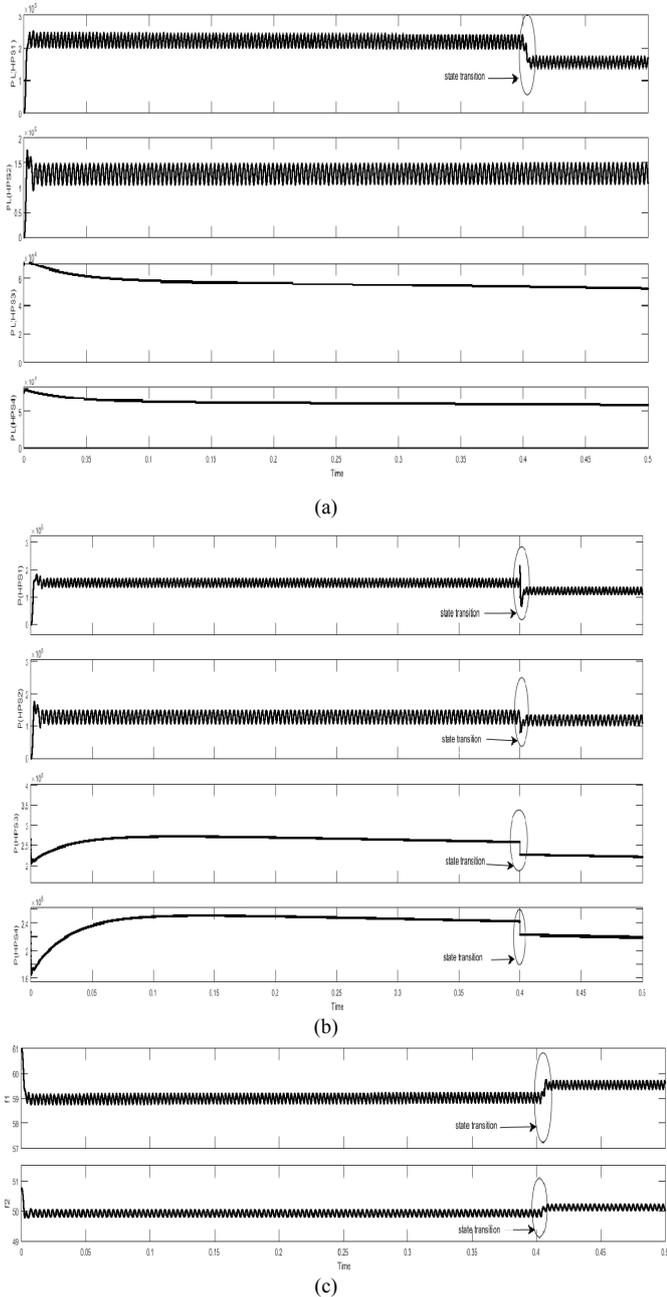


Fig. 7. Simulation results of intertied hybrid system with coordinated control (a) Load power for decrease in load on HPS₁ (b) Source power for decrease in load on HPS₁ (c) Frequency for decrease in load on HPS₁

To show further the viability of the proposed power management method, the dynamics of the intertied hybrid system are tested with decrease in load on HPS₃. Figure 9 is the outcome with the proposed control. As evident from Figure 9

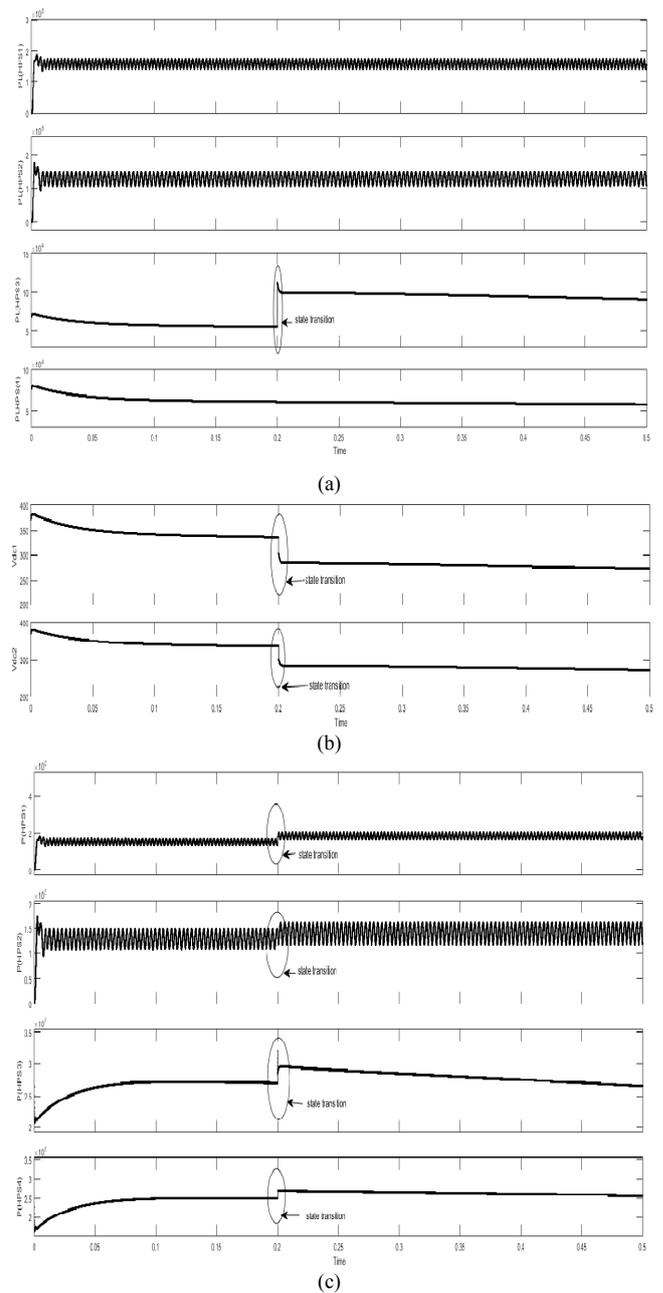


Fig. 8. Simulation results of intertied hybrid system with coordinated control (a) Load power for increase in load on HPS₃ (b) Source power for increase in load on HPS₃ (c) Frequency for increase in load on HPS₃

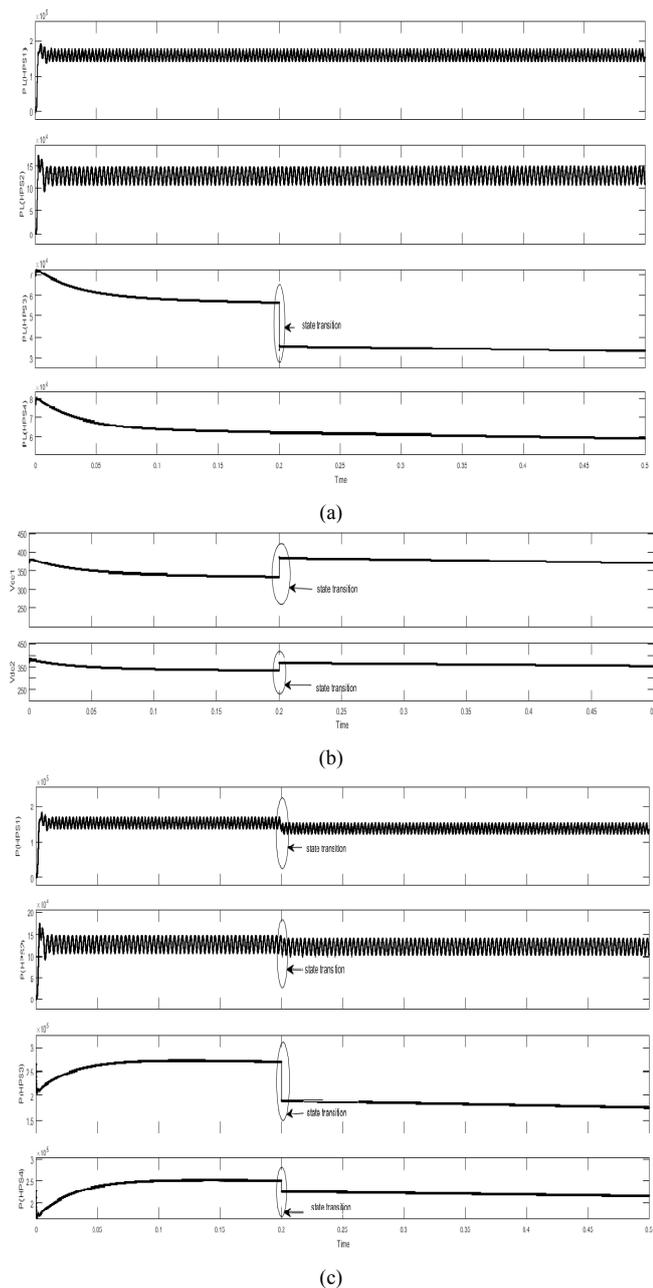


Fig. 9. Simulation results of Intertied Hybrid system with coordinated control (a) Load power for decrease in load on HPS₃ (b) Source power for decrease in load on HPS₃ (c) Frequency for decrease in load on HPS₃

The above mentioned results clearly show that the proposed power management method works well under different conditions and effective coordination among all HPS is achieved.

VI. CONCLUSION

A new control technique for an intertied hybrid AC/DC system with numerous HPSs is proposed. The proposed

decentralized power management method achieves good coordination among all HPSs. In respect to common bus a P_{dc} - V_{dc}^2 droop control strategy is proposed which maintains the common bus voltage and realizes power sharing in the storage HPS. Also the coordinated power control strategy ensures interaction among multiple HPSs to look after the common bus voltage, AC frequency and DC voltage for the individual IPCs and to ensure proper power sharing. The results with coordinated control and without coordinated control are compared. The proposed control takes system capacities into consideration, which ensures the proper supply-demand and power quality of the intertied HPS.

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